



Attorney Docket No. SAM-0274

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Min-su Kim, *et al.*

Examiner: Hu, S.

Serial No.: 09/994,146

Group Art Unit: 2811

Filing Date: November 26, 2001

Title: SEMICONDUCTOR DEVICE HAVING SILICON-ON-INSULATOR
STRUCTURE AND METHOD OF FABRICATING THE SAME

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to BOX AF, Assistant Commissioner for Patents, Washington, DC 20231.

3-3-03

Date

Vanessa Marakas

Vanessa Marakas

BOX AF

Assistant Commissioner for Patents

Washington, DC 20231

AMENDMENT AFTER FINAL REJECTION

Sir:

The following is in response to the Office Action dated December 3, 2002. Please amend the application as follows.

In the claims:

Please add the following new claims:

18. (New) A semiconductor device having a silicon-on-insulator (SOI) structure, comprising:
- an insulating layer;
 - an insular silicon region having first conductivity-type impurity ions formed on

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01 FC:1201 84.00 CH
02 FC:1202 288.00 CH